| Ref<br>#  | Hits | Search Query                                                               | DBs                                                              | Default<br>Operator | Plurals | Time Stamp       |
|-----------|------|----------------------------------------------------------------------------|------------------------------------------------------------------|---------------------|---------|------------------|
| S1        | 65   | scheduler same sram same dram                                              | US-PGPUB;<br>USPAT;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | ADJ                 | ON      | 2007/11/14 17:19 |
| S2        | 29   | S1 same queue                                                              | US-PGPUB;<br>USPAT;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | ADJ                 | ON      | 2007/11/14 15:01 |
| <b>S3</b> | 4    | S2 and hierarch\$4                                                         | US-PGPUB;<br>USPAT;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | ADJ                 | ON      | 2007/11/14 18:25 |
| S4        | 100  | (queue near2 manage\$5) same<br>sram same dram                             | US-PGPUB;<br>USPAT;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | ADJ                 | ON      | 2007/11/14 15:01 |
| S5        | 46   | S4 and hierarch\$4                                                         | US-PGPUB;<br>USPAT;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | ADJ                 | ON      | 2007/11/14 16:46 |
| S6        |      | S1 same (enqueue or dequeue)                                               | US-PGPUB;<br>USPAT;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | ADJ                 | ON      | 2007/11/14 15:16 |
| S7        | 3    | CELL/FRAME SCHEDULING METHOD<br>AND COMMUNICATIONS<br>CELL/FRAME SCHEDULER | US-PGPUB;<br>USPAT;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | ADJ                 | ON      | 2007/11/14 15:14 |
| S8        | 8081 | dram with read only                                                        | US-PGPUB;<br>USPAT;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | ADJ                 | ON      | 2007/11/14 16:32 |

|     |      |                                                                          | т                                                                |     | ı — — — |                  |
|-----|------|--------------------------------------------------------------------------|------------------------------------------------------------------|-----|---------|------------------|
| S9  | 7    | S5 and S8                                                                | US-PGPUB;<br>USPAT;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | ADJ | ON      | 2007/11/14 16:31 |
| S10 | . 1  | S8 same (enqueue or dequeue)                                             | US-PGPUB;<br>USPAT;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | ADJ | ON      | 2007/11/14 16:33 |
| S11 | 43   | S5 and (@ad<="20050205" or<br>@rlad<="20050205" or<br>@prad<="20050205") | US-PGPUB;<br>USPAT;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | ADJ | ON      | 2007/11/14 16:49 |
| S12 | 43   | S11 and pipeline                                                         | US-PGPUB;<br>USPAT;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | ADJ | ON      | 2007/11/14 16:47 |
| S13 | 35   | S11 and (enqueue or dequeue)                                             | US-PGPUB;<br>USPAT;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | ADJ | ON      | 2007/11/14 16:50 |
| S14 | 3762 | read-modify-write                                                        | US-PGPUB;<br>USPAT;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | ADJ | ON      | 2007/11/14 16:52 |
| S15 | 0    | S14 and S13                                                              | US-PGPUB;<br>USPAT;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | ADJ | ON .    | 2007/11/14 16:51 |
| S16 | 0    | S14 and S11                                                              | US-PGPUB;<br>USPAT;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | ADJ | ON      | 2007/11/14 16:51 |

| S17   | 5748 | read near3 modify near3 write               | US-PGPUB;                                                        | ADJ    | ON | 2007/11/14 16:52 |
|-------|------|---------------------------------------------|------------------------------------------------------------------|--------|----|------------------|
|       |      | ,                                           | USPAT;<br>FPRS;<br>EPO; JPO;                                     | ,,,,,, |    |                  |
|       |      |                                             | DERWENT;<br>IBM_TDB                                              |        |    |                  |
| S18 . | 1    | S11 and S17                                 | US-PGPUB;<br>USPAT;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | ADJ    | ON | 2007/11/14 16:53 |
| S19   | 0    | S13 and S17                                 | US-PGPUB;<br>USPAT;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | ADJ .  | ON | 2007/11/14 16:52 |
| S20   | 21   | sram same dram same enqueue<br>same dequeue | US-PGPUB;<br>USPAT;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | ADJ    | ON | 2007/11/14 18:23 |
| S21   |      | S20 and S14                                 | US-PGPUB;<br>USPAT;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | ADJ    | ON | 2007/11/14 17:30 |
| S22   | 3762 | S17 and S14                                 | US-PGPUB;<br>USPAT;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | ADJ    | ON | 2007/11/14 17:29 |
| S23   | 3    | S17 and S20                                 | US-PGPUB;<br>USPAT;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | ADJ    | ON | 2007/11/14 17:31 |
| S24   |      | S20 and hierarch\$4                         | US-PGPUB;<br>USPAT;<br>FPRS;<br>EPO; JPO;                        | ADJ    | ON | 2007/11/14 18:21 |
|       |      |                                             | DERWENT;<br>IBM_TDB                                              |        |    |                  |

| S25 | 28  | sram same dram same (read near2 modify near2 write)                                               | US-PGPUB;<br>USPAT;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | ADJ | ON | 2007/11/14 18:24 |
|-----|-----|---------------------------------------------------------------------------------------------------|------------------------------------------------------------------|-----|----|------------------|
| S26 | 1   | S25 same (queue manage\$3 or schedule\$3 or classif\$3)                                           | US-PGPUB;<br>USPAT;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | ADJ | ON | 2007/11/14 18:25 |
| S27 | 7   | S25 and (queue manage\$3 or schedule\$3 or classif\$3)                                            | US-PGPUB;<br>USPAT;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | ADJ | ON | 2007/11/14 18:39 |
| S28 | 1   | S27 and hierarch\$4                                                                               | US-PGPUB;<br>USPAT;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | ADJ | ON | 2007/11/14 18:26 |
| S29 | 2   | S25 and hierarch\$4                                                                               | US-PGPUB;<br>USPAT;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | ADJ | ON | 2007/11/14 18:26 |
| S30 | 0   | Parallel asynchronous propagation pipeline structure and methods to access multiple memory arrays | US-PGPUB;<br>USPAT;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | ADJ | ON | 2007/11/14 18:39 |
| S31 | . 2 | "6922732".pn.                                                                                     | US-PGPUB;<br>USPAT;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | ADJ | ON | 2007/11/15 14:09 |
| S32 | 2   | "6035372".pn.                                                                                     | US-PGPUB;<br>USPAT;<br>FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | ADJ | ON | 2007/11/16 07:18 |

| S33 | 2 | "20060206684" | US-PGPUB;<br>USPAT;                       | ADJ | ON | 2007/11/16 07:18 |
|-----|---|---------------|-------------------------------------------|-----|----|------------------|
|     |   |               | FPRS;<br>EPO; JPO;<br>DERWENT;<br>IBM TDB |     |    |                  |



## PALM INTRANET

Day : Friday Date: 11/16/2007

Time: 09:58:04

#### **Inventor Name Search Result**

Your Search was:

Last Name = BASSO First Name = CLAUDE

|                 |                |        | r          |                                                                                                                                                                               |               |
|-----------------|----------------|--------|------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|---------------|
| Application#    | Patent#        | Status | Date Filed | Title                                                                                                                                                                         | Inventor Name |
|                 | 5148527        |        |            | INTEFERENCE FOR INDEPENDENTLY ESTABLISHING A LINK AND TRANSMITTING HIGH LEVEL COMMANDS INCLUDING LOGICAL ADDRESSES FROM DEDICATED MICROPROCESSOR TO SHARED INTELLIGENT MEMORY | BASSO, CLAUDE |
| <u>07641833</u> | 5235700        | 150    | 01/16/1991 | CHECKPOINTING MECHANISM<br>FOR FAULT-TOLERANT<br>SYSTEMS                                                                                                                      | BASSO, CLAUDE |
| 07862931        | Not<br>Issued  | 163    | 04/03/1992 | METHOD AND APPARATUS<br>FOR PERFORMING PATTERN<br>SEARCH FUNCTIONS                                                                                                            | BASSO, CLAUDE |
| 08018618        | Not<br>Issued  | 166    |            | HUB AND INTERFACE FOR ISOCHRONOUS TOKEN-RING                                                                                                                                  | BASSO, CLAUDE |
|                 | 5491815        | 150    | 09/10/1993 | METHOD AND DEVICE FOR<br>CONTROLLING TIMERS<br>ASSOCIATED WITH MULTIPLE<br>USERS IN A DATA PROCESSING<br>SYSTEM                                                               | BASSO, CLAUDE |
| 08161354        | Not<br>Issued  | 161    | 12/02/1993 | TOKEN STAR SWITCH                                                                                                                                                             | BASSO, CLAUDE |
| 08161372        | 5444692        | 250    | 12/02/1993 | TOKEN STAR BRIDGE                                                                                                                                                             | BASSO, CLAUDE |
| 08234193        | <u>5561807</u> | 150    | : I        | METHOD AND DEVICE OF<br>MULTICASTING DATA IN A<br>COMMUNICATIONS SYSTEM                                                                                                       | BASSO, CLAUDE |
| 08547635        | 5794033        | 150    |            | METHOD AND SYSTEM FOR INSITE AND ON-LINE REPROGRAMMING OF HARDWARE LOGICS WITH REMOTE LOADING IN A                                                                            | BASSO, CLAUDE |

|                |        |            | NETWORK DEVICE                                                                                                  |               |
|----------------|--------|------------|-----------------------------------------------------------------------------------------------------------------|---------------|
| Not<br>Issued  | 166    | 10/25/1995 | EFFICIENT<br>HARDWARE/SOFTWARE<br>INTERFACE FOR A DATA<br>SWITCH                                                | BASSO, CLAUDE |
| 5673263        | 150    | 10/25/1995 | METHOD FOR USING AN IP<br>ADDRESS-BASED ROUTING<br>PROTOCOL IN AN ATM<br>ENVIRONMENT                            | BASSO, CLAUDE |
| 5787071        | 150    | 11/06/1995 | HOP-BY-HOP FLOW CONTROL<br>IN AN ATM NETWORK                                                                    | BASSO, CLAUDE |
| <u>5687356</u> | 250    | 12/27/1995 | HUB AND INTERFACE FOR ISOCHRONOUS TOKEN RING                                                                    | BASSO, CLAUDE |
| 6038592        | 150    | 04/19/1996 | METHOD AND DEVICE OF<br>MULTICASTING DATA IN A<br>COMMUNICATIONS SYSTEM                                         | BASSO, CLAUDE |
| 5724348        | 150    | 05/05/1997 | EFFICIENT<br>HARDWARE/SOFTWARE<br>INTERFACE FOR A DATA<br>SWITCH                                                | BASSO, CLAUDE |
| 6104715        | 150    | 11/19/1997 | MERGING OF DATA CELLS IN<br>AN ATM NETWORK                                                                      | BASSO, CLAUDE |
| 6370119        | 150    | 03/06/1998 | COMPUTING THE WIDEST<br>SHORTEST PATH IN HIGH-<br>SPEED NETWORKS                                                | BASSO, CLAUDE |
| 6195335        | 150    | 07/06/1998 | DATA SWITCH                                                                                                     | BASSO, CLAUDE |
| Not<br>Issued  | 161    | 06/16/1999 | SUPPORT OF NON-<br>HIERARCHICAL PNNI NODES<br>IN A HIERARCHICAL PNNI ATM<br>NETWORK                             | BASSO, CLAUDE |
| 7002906        | 150    | 06/16/1999 | METHOD FOR EXTENDING THE<br>CRANKBACK PROCEDURE TO<br>ANY ASYNCHRONOUS<br>TRANSFER MODE SYSTEM                  | BASSO, CLAUDE |
| 7106698        | 150    | 06/16/1999 | SYSTEM FOR TRIGGERING THE<br>CONTROL PLANE IN AN<br>ASYNCHRONOUS<br>CONNECTION-ORIENTED<br>TRANSMISSION NETWORK | BASSO, CLAUDE |
| 6633539        | 150    | 07/12/1999 | DEVICE, METHOD AND<br>ARTICLE OF MANUFACTURE<br>FOR CALL SETUP PACING IN<br>CONNECTION-ORIENTED<br>NETWORKS     | BASSO, CLAUDE |
|                | Issued | Issued     | Issued                                                                                                          | Issued        |

| 09419316        | Not<br>Issued | 161 |            | OVERSUBSCRIPTION WITH<br>VIRTUAL PATHS IN ATM<br>NETWORKS                                                                                                              | BASSO, CLAUDE |
|-----------------|---------------|-----|------------|------------------------------------------------------------------------------------------------------------------------------------------------------------------------|---------------|
| 09437820        | 6690678       | 150 |            | METHOD AND SYSTEM IN A PACKET SWITCHING NETWORK FOR DYNAMICALLY ADJUSTING THE BANDWIDTH OF A CONTINUOUS BIT RATE VIRTUAL PATH CONNECTION ACCORDING TO THE NETWORK LOAD | BASSO, CLAUDE |
| 09544312        | 6658481       | 150 | 04/06/2000 | A ROUTER USES A SINGLE HIERARCHY INDEPENDENT ROUTING TABLE THAT INCLUDES A FLAG TO LOOK- UP A SERIES OF NEXT HOP ROUTERS FOR ROUTING PACKETS                           | BASSO, CLAUDE |
| 09544314        | 6996823       | 150 | 04/06/2000 | INTER PROCESS COMMUNICATIONS IN A DISTRIBUTED CP AND NP ENVIRONMENT                                                                                                    | BASSO, CLAUDE |
| 09544588        | 6675221       | 150 | 04/06/2000 | METHOD AND APPARATUS<br>FOR CUSTOMIZING AND<br>FORWARDING PARAMETERS<br>IN A NETWORK PROCESSOR                                                                         | BASSO, CLAUDE |
| 09546133        | 6880158       | 150 | 04/10/2000 | NETWORK PROCESSOR<br>SERVICES ARCHITECTURE<br>THAT IS PLATFORM AND<br>OPERATING SYSTEM<br>INDEPENDENT                                                                  | BASSO, CLAUDE |
| <u>09546702</u> | 6721800       | 150 | 04/10/2000 | A SYSTEM USING WEIGHTED NEXT HOP OPTION IN ROUTING TABLE TO INCLUDE PROBABILITY OF ROUTING A PACKET FOR PROVIDING EQUAL COST MULTIPATH FORWARDING PACKETS              | BASSO, CLAUDE |
| 09547362        | 6763375       | 150 | 04/11/2000 | METHOD FOR DEFINING AND<br>CONTROLLING THE OVERALL<br>BEHAVIOR OF A NETWORK<br>PROCESSOR DEVICE                                                                        | BASSO, CLAUDE |
| 09621401        | 6668261       | 150 | 07/21/2000 | METHOD OF UPGRADING A<br>PROGRAM USING ASSOCIATED<br>CONFIGURATION DATA                                                                                                | BASSO, CLAUDE |
| 09625624        | 7177951       | 150 | 07/26/2000 | ADDRESS MANAGEMENT IN<br>PNNI HIERARCHICAL                                                                                                                             | BASSO, CLAUDE |

| 09633767<br>09766074 | Not     | 71  | 1          |                                                                                                                                                         |               |
|----------------------|---------|-----|------------|---------------------------------------------------------------------------------------------------------------------------------------------------------|---------------|
| 09766074             | Issued  |     |            | Alternative routing in PNNI hierarchical networks                                                                                                       | BASSO, CLAUDE |
|                      | 6907007 | 150 | 01/19/2001 | METHOD OF<br>INJECTING/EXTRACTING<br>CONTROL CELLS IN AN<br>ASYNCHRONOUS TRANSFER<br>MODE (ATM) NETWORK                                                 | BASSO, CLAUDE |
| 09828342             | 7200696 | 150 | 04/06/2001 | SYSTEM METHOD STRUCTURE IN NEWORK PROCESSOR THAT INDICATES LAST DATA BUFFER OF FRAME PACKET BY LAST FLAG BIT THAT IS EITHER IN FRIST OR SECOND POSITION | BASSO, CLAUDE |
| 09838916             | 6473434 | 150 | 04/20/2001 | SCALEABLE AND ROBUST SOLUTION FOR REDUCING COMPLEXITY OF RESOURCE IDENTIFIER DISTRIBUTION IN A LARGE NETWORK PROCESSOR-BASED SYSTEM                     | BASSO, CLAUDE |
| 09839010             | 6937606 | 150 | 04/20/2001 | DATA STRUCTURES FOR<br>EFFICIENT PROCESSING OF IP<br>FRAGMENTATION AND<br>REASSEMBLY                                                                    | BASSO, CLAUDE |
| 09839079             | 6836480 | 150 | 04/20/2001 | DATA STRUCTURES FOR<br>EFFICIENT PROCESSING OF<br>MULTICAST TRANSMISSIONS                                                                               | BASSO, CLAUDE |
| 09864971             | 6987735 | 150 | 05/24/2001 | SYSTEM AND METHOD FOR<br>ENHANCING THE<br>AVAILABILITY OF ROUTING<br>SYSTEMS THROUGH EQUAL<br>COST MULTIPATH                                            | BASSO, CLAUDE |
| 09885782             | 7003582 | 150 | 06/20/2001 | ROBUST NP-BASED DATA<br>FORWARDING TECHNIQUES<br>THAT TOLERATE FAILURE OF<br>CONTROL-BASED<br>APPLICATIONS                                              | BASSO, CLAUDE |
| 09898253             | 7054855 | 150 | 07/03/2001 | METHOD AND SYSTEM FOR<br>PERFORMING A PATTERN<br>MATCH SEARCH FOR TEXT<br>STRINGS                                                                       | BASSO, CLAUDE |
| 09931206             | 7065086 | 150 | 08/16/2001 | METHOD AND SYSTEM FOR<br>EFFICIENT LAYER 3-LAYER 7<br>ROUTING OF INTERNET<br>PROTOCOL ("IP") FRAGMENTS                                                  | BASSO, CLAUDE |

| <u>10144075</u> | 7280527       | 150 | 05/13/2002 | LOGICALLY GROUPING<br>PHYSICAL PORTS INTO<br>LOGICAL INTERFACES TO<br>EXPAND BANDWIDTH                                       | BASSO, CLAUDE |
|-----------------|---------------|-----|------------|------------------------------------------------------------------------------------------------------------------------------|---------------|
| <u>10155675</u> | 6973503       | 150 | 05/23/2002 | PREVENTING AT LEAST IN<br>PART CONTROL PROCESSORS<br>FROM BEING OVERLOADED                                                   | BASSO, CLAUDE |
| 10174390        | 7304991       | 150 | 06/18/2002 | MINIMIZING MEMORY<br>ACCESSES FOR A NETWORK<br>IMPLEMENTING DIFFERENTIAL<br>SERVICES OVER MULTI-<br>PROTOCOL LABEL SWITCHING | BASSO, CLAUDE |
| 10191657        | 7260096       | 150 | 07/09/2002 | METHOD AND ROUTER FOR<br>FORWARDING INTERNET<br>DATA PACKETS                                                                 | BASSO, CLAUDE |
| 10217529        | 7036125       | 150 | 08/13/2002 | ELIMINATING MEMORY<br>CORRUPTION WHEN<br>PERFORMING TREE<br>FUNCTIONS ON MULTIPLE<br>THREADS                                 | BASSO, CLAUDE |
| 10219643        | Not<br>Issued | 93  | 08/15/2002 | DATABASE MANAGEMENT<br>SYSTEM AND METHOD OF<br>USING IT TO TRANSMIT<br>PACKETS                                               | BASSO, CLAUDE |
| 10230921        | Not<br>Issued | 95  | 08/29/2002 | METHOD AND SYSTEM FOR<br>REDUCING LOOK-UP TIME IN<br>PACKET FORWARDING ON<br>COMPUTER NETWORKS                               | BASSO, CLAUDE |
| 10364069        | Not<br>Issued | 83  |            | Frame alteration logic for network processors                                                                                | BASSO, CLAUDE |

| Search Another: Inven  | Last Name | First Name |        |
|------------------------|-----------|------------|--------|
| Scarcii Another. Inven | BASSO     | CLAUDE     | Search |

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# PALM INTRANET

Day : Friday Date: 11/16/2007

Time: 09:58:08

#### **Inventor Name Search Result**

Your Search was:

Last Name = CALVIGNAC

First Name = JEAN

| Application# | Patent# | Status | Date Filed | Title                                                                                                          | Inventor Name      |
|--------------|---------|--------|------------|----------------------------------------------------------------------------------------------------------------|--------------------|
| 06433609     | 4493051 | 150    | 10/12/1982 | COMMUNICATION LINE<br>SCANNING DEVICE FOR A<br>COMMUNICATION<br>CONTROLLER                                     | CALVÍGNAC,<br>JEAN |
| 06433744     | 4491913 | 150    | 10/12/1982 | ADDRESS GENERATING<br>DEVICE FOR A<br>COMMUNICATION LINE<br>SCANNING DEVICE                                    | CALVIGNAC,<br>JEAN |
| 06892649     | 4763321 | 150    | 08/04/1986 | DYNAMIC BANDWIDTH ALLOCATION MECHANISM BETWEEN CIRCUIT SLOTS AND PACKET BIT STREAM IN A COMMUNICATION NETWORK  | CALVIGNAC,<br>JEAN |
| 06893073     | 4761781 | 150    | 08/04/1986 | ADAPTATIVE PACKET/CIRCUIT SWITCHED TRANSPORTATION METHOD AND SYSTEM                                            | CALVIGNAC,<br>JEAN |
| 07077484     | 4799219 | 150    | 07/24/1987 | PSEUDO SYNCHRONOUS<br>TRANSPORT MECHANISM IN<br>A COMMUNICATION<br>NETWORK                                     | CALVIGNAC,<br>JEAN |
| 07077485     | 4819230 | 150    | 07/24/1987 | OPTIMIZED BANDWITH ALLOCATION MECHANISM BETWEEN CIRCUIT SLOTS AND PACKET BIT STREAM IN A COMMUNICATION NETWORK | CALVIGNAC,<br>JEAN |
| 07259025     | 5046069 | 250    |            | DATA INTEGRITY SECURING<br>MEANS                                                                               | CALVIGNAC,<br>JEAN |
| 07266961     | 4964127 | 150    | 11/03/1988 | DATA VALIDITY CHECKING<br>MEANS                                                                                | CALVIGNAC,<br>JEAN |
| 07357225     | 5119478 | 150    | 05/26/1989 | PARALLEL PROCESSING<br>METHOD AND DEVICE FOR                                                                   | CALVIGNAC,<br>JEAN |

|                 |                |     |            | RECEIVING AND<br>TRANSMITTING HDLC SDLC<br>BIT STREAMS                                                                  |                    |
|-----------------|----------------|-----|------------|-------------------------------------------------------------------------------------------------------------------------|--------------------|
| 07416165        | Not<br>Issued  | 166 | 10/02/1989 | MECHANISM FOR TRANSFERRING MESSAGES BETWEEN SOURCE AND DESTINATION USERS THROUGH A SHARED MEMORY                        | CALVIGNAC,<br>JEAN |
| 07479139        | Not<br>Issued  | 166 | 02/13/1990 | SWITCHING SYSTEM FOR<br>SIMULTANEOUSLY<br>TRANSFERRING DATA<br>BETWEEN DATA PROCESSING<br>UNITS                         | CALVIGNAC,<br>JEAN |
| <u>07506035</u> | 5119376        | 150 | 04/06/1990 | INTERCONNECTION SYSTEM FOR THE ATTACHMENT OF USER EQUIPMENTS TO A COMMUNICATION PROCESSING UNIT                         | CALVIGNAC,<br>JEAN |
| <u>07576876</u> | 5197065        | 150 | 09/04/1990 | DISTRIBUTION MECHANISM<br>FOR ESTABLISHING<br>COMMUNICATIONS<br>BETWEEN USER INTERFACES<br>OF A COMMUNICATION<br>SYSTEM | CALVIGNAC,<br>JEAN |
| 07641833        | 5235700        | 150 | 01/16/1991 | CHECKPOINTING<br>MECHANISM FOR FAULT-<br>TOLERANT SYSTEMS                                                               | CALVIGNAC,<br>JEAN |
| <u>07657906</u> | 5134636        | 150 | 02/20/1991 | SYNCHRONIZATION CIRCUIT<br>FOR A SYNCHRONOUS<br>SWITCHING SYSTEM                                                        | CALVIGNAC,<br>JEAN |
| 07684111        | <u>5251206</u> | 150 | 04/12/1991 | HYBRID SWITCHING SYSTEM<br>FOR A COMMUNICATION<br>NODE                                                                  | CALVIGNAC,<br>JEAN |
| 07740356        | Not<br>Issued  | 163 | 08/05/1991 | MECHANISM FOR PERFORMING THE STEADY STATE AND ERROR RECOVERY FUNCTIONS OF A COMMUNICATION PROTOCOL                      | CALVIGNAC,<br>JEAN |
| 07830128        | 5237572        |     |            | ACTIVE REMOTE MODULE FOR THE ATTACHMENT OF USER EQUIPMENTS TO A COMMUNICATION PROCESSING UNIT                           | CALVIGNAC,<br>JEAN |
| <u>07836492</u> | 5325404        | 150 |            | SYNCHRONIZATION DEVICE<br>FOR PERFORMING                                                                                | CALVIGNAC,<br>JEAN |

|          |               |     |            | SYNCHRONOUS CIRCUIT SWITCHING FUNCTIONS THRU AN ASYNCHRONOUS COMMUNICATION NODE                                                                       |                    |
|----------|---------------|-----|------------|-------------------------------------------------------------------------------------------------------------------------------------------------------|--------------------|
| 07862931 | Not<br>Issued | 163 | 04/03/1992 | METHOD AND APPARATUS<br>FOR PERFORMING PATTERN<br>SEARCH FUNCTIONS                                                                                    | CALVIGNAC,<br>JEAN |
| 07903687 | 5341475       | 150 |            | METHOD FOR EXCHANGING MESSAGES BETWEEN A SHARED MEMORY AND COMMUNICATION ADAPTERS USING AN EFFICIENT LOGICAL PROTOCOL                                 | CALVIGNAC,<br>JEAN |
| 07909884 | 5432910       | 150 | 07/07/1992 |                                                                                                                                                       | CALVIGNAC,<br>JEAN |
| 07965554 | 5333269       | 250 | 10/23/1992 | MECHANISM FOR<br>TRANSFERRING MESSAGES<br>BETWEEN SOURCE AND<br>DESTINATION USERS<br>THROUGH A SHARED<br>MEMORY                                       | CALVIGNAC,<br>JEAN |
| 07992314 | 5572697       | 250 | 12/21/1992 | APPARATUS FOR<br>RECOVERING LOST BUFFER<br>CONTENTS IN A DATA<br>PROCESSING SYSTEM                                                                    | CALVIGNAC,<br>JEAN |
| 08005425 | 5392401       | 250 | 01/21/1993 | SWITCHING SYSTEM FOR<br>SIMULTANEOUSLY<br>TRANSFERRING DATA<br>BETWEEN DATA PROCESSING<br>UNITS                                                       | CALVIGNAC,<br>JEAN |
| 08018618 | Not<br>Issued | 166 | 02/17/1993 | HUB AND INTERFACE FOR ISOCHRONOUS TOKEN-RING                                                                                                          | CALVIGNAC,<br>JEAN |
| 08024061 | 5467359       | 150 | 03/01/1993 | APPARATUS FOR GENERATING AND CHECKING THE ERROR CORRECTION CODES OF MESSAGES IN A MESSAGE SWITCHING SYSTEM                                            | CALVIGNAC,<br>JEAN |
| 08036294 | 5530903       | 150 | 03/24/1993 | SYSTEM FOR REASSIGNING A<br>HIGHER PRIORITY TO AN<br>INTERRUPTED USER BY<br>INHIBITING THE ACCESS OF<br>OTHER USERS UNTIL THE<br>INTERRUPTED USER HAS | CALVIGNAC,<br>JEAN |

|          | 1 1           |     |            | COMPLETED ITS TASK                                                                                                 | ĺ                  |
|----------|---------------|-----|------------|--------------------------------------------------------------------------------------------------------------------|--------------------|
| 08043686 | Not<br>Issued | 161 |            |                                                                                                                    | CALVIGNAC,<br>JEAN |
| 08120112 | 5491815       | 150 | 09/10/1993 | l l                                                                                                                | CALVIGNAC,<br>JEAN |
| 08161354 | Not<br>Issued | 161 | 12/02/1993 |                                                                                                                    | CALVIGNAC,<br>JEAN |
| 08161372 | 5444692       | 250 | 12/02/1993 | TOKEN STAR BRIDGE                                                                                                  | CALVIGNAC,<br>JEAN |
| 08206363 | 5557266       | 150 | 03/07/1994 | SYSTEM FOR CASCADING<br>DATA SWITCHES IN A<br>COMMUNICATION NODE                                                   | CALVIGNAC,<br>JEAN |
| 08234193 | 5561807       | 150 | 04/28/1994 | METHOD AND DEVICE OF<br>MULTICASTING DATA IN A<br>COMMUNICATIONS SYSTEM                                            | CALVIGNAC,<br>JEAN |
| 08446997 | 5557608       | 150 | 05/22/1995 | METHOD AND APPARATUS FOR TRANSMISSION OF HIGH PRIORITY TRAFFIC ON LOW SPEED COMMUNICATION LINKS                    | CALVIGNAC,<br>JEAN |
| 08547635 | 5794033       | 150 | 10/24/1995 | METHOD AND SYSTEM FOR IN-SITE AND ON-LINE REPROGRAMMING OF HARDWARE LOGICS WITH REMOTE LOADING IN A NETWORK DEVICE | CALVIGNAC,<br>JEAN |
| 08547825 | Not<br>Issued | 166 | 10/25/1995 | EFFICIENT<br>HARDWARE/SOFTWARE<br>INTERFACE FOR A DATA<br>SWITCH                                                   | CALVIGNAC,<br>JEAN |
| 08547826 | 5666361       | 150 | 10/25/1995 | ATM CELL FORWARDING<br>AND LABEL SWAPPING<br>METHOD AND APPARATUS                                                  | CALVIGNAC,<br>JEAN |
| 08547827 | 5668798       | 150 | 10/25/1995 | MULTIPLEXED TC SUBLAYER<br>FOR ATM SWITCH                                                                          | CALVIGNAC,<br>JEAN |
| 08547828 | 5768273       | 150 | 10/25/1995 | METHOD AND APPARATUS<br>FOR PRIORITY LEVEL<br>QUEUEING IN PROCESSING<br>ATM CELL HEADER AND<br>PAYLOAD             | CALVIGNAC,<br>JEAN |

| 08548030 | 5684797 | 150 |            | ATM CELL MULTICASTING<br>METHOD AND APPARATUS                                                                          | CALVIGNAC,<br>JEAN |
|----------|---------|-----|------------|------------------------------------------------------------------------------------------------------------------------|--------------------|
| 08554113 | 5787071 | 150 | 11/06/1995 | HOP-BY-HOP FLOW CONTROL<br>IN AN ATM NETWORK                                                                           | CALVIGNAC,<br>JEAN |
| 08570840 | 5629928 | 150 | 12/12/1995 | DYNAMIC FAIR QUEUING TO<br>SUPPORT BEST EFFORT<br>TRAFFIC IN AN ATM<br>NETWORK                                         | CALVIGNAC,<br>JEAN |
| 08579555 | 5687356 | 250 | 12/27/1995 | HUB AND INTERFACE FOR ISOCHRONOUS TOKEN RING                                                                           | CALVIGNAC,<br>JEAN |
| 08635048 | 6038592 | 150 | 04/19/1996 | METHOD AND DEVICE OF<br>MULTICASTING DATA IN A<br>COMMUNICATIONS SYSTEM                                                | CALVIGNAC,<br>JEAN |
| 08786914 | 5946297 | 150 | 01/22/1997 | N SCHEDULING METHOD<br>AND APPARATUS FOR<br>SUPPORTING ATM<br>CONNECTIONS HAVIN G A<br>GUARANTEED MINIMUN<br>BANDWIDTH | CALVIGNAC,<br>JEAN |
| 08824941 | 5923664 | 150 |            | METHOD AND APPARATUS<br>PROVIDING A MULTIPORT<br>PHYSICAL INTERFACE TO<br>HIGH SPEED PACKET<br>NETWORKS                | CALVIGNAC,<br>JEAN |
| 08858156 | 5724348 | 150 | 05/05/1997 | EFFICIENT<br>HARDWARE/SOFTWARE<br>INTERFACE FOR A DATA<br>SWITCH                                                       | CALVIGNAC,<br>JEAN |
| 08991911 | 6144637 | 150 | 12/16/1997 | DATA COMMUNICATIONS                                                                                                    | CALVIGNAC,<br>JEAN |
| 08993695 | 6003060 | 250 | 12/18/1997 | METHOD AND APPARATUS<br>TO SHARE RESOURCES<br>WHILE PROCESSING<br>MULTIPLE PRIORITY DATA<br>FLOWS                      | CALVIGNAC,<br>JEAN |

| Saarah Anathari Inventor | Last Name | First Name |        |
|--------------------------|-----------|------------|--------|
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# PALM INTRANET

# Inventor Name Search Result

Your Search was:

Last Name = CHANG First Name = CHIH-JEN

| Application#    | Patent#       | Status | Date Filed | Title                                                                                                                      | Inventor Name   |  |
|-----------------|---------------|--------|------------|----------------------------------------------------------------------------------------------------------------------------|-----------------|--|
| 09191328        | 6198535       | 250    | 11/13/1998 | WAFER ALIGNMENT SYSTEM                                                                                                     | CHANG, CHIH-JEN |  |
| 09193220        | 6166392       | 150    | 11/16/1998 | EXPOSURE FOR<br>PERFORMING<br>SYNCHRONIZED OFF-AXIS<br>ALIGNMENT                                                           | CHANG, CHIH-JEN |  |
| 10233989        | Not<br>Issued | 120    | 09/02/2002 | Method for active queue management with asymmetric congestion control                                                      | CHANG, CHIH-JEN |  |
| <u>10235672</u> | Not<br>Issued | 168    | 09/05/2002 | Receive queue device with efficient queue flow control, segment placement and virtualization mechanisms                    | CHANG, CHIH-JEN |  |
| 10235689        | Not<br>Issued | 133    | 09/05/2002 | Split socket send queue apparatus and method with efficient queue flow control, retransmission and sack support mechanisms | CHANG, CHIH-JEN |  |
| 10772737        | Not<br>Issued | 30     | 02/05/2004 | Structure and method for scheduler pipeline design for hierarchical link sharing                                           | CHANG, CHIH-JEN |  |
| 10868725        | Not<br>Issued | 41     | 06/15/2004 | Method and structure for enqueuing data packets for processing                                                             | CHANG, CHIH-JEN |  |
| 10899937        | 7277982       | 150    | 07/27/2004 | DRAM ACCESS COMMAND<br>QUEUING STRUCTURE                                                                                   | CHANG, CHIH-JEN |  |
| 11030344        | Not<br>Issued | 30     | 01/06/2005 | Apparatus and method for efficiently modifying network data frames                                                         | CHANG, CHIH-JEN |  |
| 11042692        | Not<br>Issued | 83     | 01/25/2005 | Systems and methods for TDM multithreading                                                                                 | CHANG, CHIH-JEN |  |
| 11062036        | Not<br>Issued | 30     | 02/18/2005 | Method for sharing single data buffer by several packets                                                                   | CHANG, CHIH-JEN |  |
| <u>11070060</u> | 7293158       | 150    | 03/02/2005 | SYSTEMS AND METHODS                                                                                                        | CHANG, CHIH-JEN |  |

|          |               |    | , ,        |                                                                                                                                              |                 |
|----------|---------------|----|------------|----------------------------------------------------------------------------------------------------------------------------------------------|-----------------|
|          |               |    |            | FOR IMPLEMENTING COUNTERS IN A NETWORK PROCESSOR WITH COST EFFECTIVE MEMORY                                                                  |                 |
| 11070758 | Not<br>Issued | 41 | 03/02/2005 | Systems and methods for adaptively mapping an instruction cache                                                                              | CHANG, CHIH-JEN |
| 11076218 | Not<br>Issued | 41 | 03/09/2005 | Systems and methods for multi-<br>frame control blocks                                                                                       | CHANG, CHIH-JEN |
| 11091245 | Not<br>Issued | 30 | 03/28/2005 | Flexible control block format for frame description and management                                                                           | CHANG, CHIH-JEN |
| 11096353 | Not<br>Issued | 30 |            | System and method for a method for reducing latency in a host ethernet adapter (HEA)                                                         | CHANG, CHIH-JEN |
| 11096362 | Not<br>Issued | 30 | 04/01/2005 | Method and system for performing a packet header lookup                                                                                      | CHANG, CHIH-JEN |
| 11096363 | Not<br>Issued | 30 | 04/01/2005 | Method and system for accommodating several ethernet ports and a wrap transmitted flow handled by a simplifed frame-by-frame upper structure | CHANG, CHIH-JEN |
| 11096365 | Not<br>Issued | 30 | 04/01/2005 | System and method for parsing, filtering, and computing the checksum in a host ethernet adapter (HEA)                                        | CHANG, CHIH-JEN |
| 11096571 | Not<br>Issued | 30 | 04/01/2005 | Method and apparatus for providing a network connection table                                                                                | CHANG, CHIH-JEN |
| 11097051 | Not<br>Issued | 30 | 04/01/2005 | Network communications for operating system partitions                                                                                       | CHANG, CHIH-JEN |
| 11097055 | Not<br>Issued | 30 | 04/01/2005 | Method and apparatus for blind checksum and correction for network transmissions                                                             | CHANG, CHIH-JEN |
| 11097430 | Not<br>Issued | 30 | 04/01/2005 | System and method for computing a blind checksum in a host ethernet adapter (HEA)                                                            | CHANG, CHIH-JEN |
| 11097608 | Not<br>Issued | 30 | 04/01/2005 | Host ethernet adapter for networking offload in server environment                                                                           | CHANG, CHIH-JEN |
| 11097652 | Not<br>Issued | 41 | 04/01/2005 | Configurable ports for a host Ethernet adapter                                                                                               | CHANG, CHIH-ÆN  |
| 11108485 | Not<br>Issued | 30 | 04/18/2005 | Systems and methods for weighted best effort scheduling                                                                                      |                 |
| 11119329 | Not           | 30 | 04/29/2005 | Systems and methods for rate-                                                                                                                | CHANG, CHIH-JEN |

|          | Issued        | 1  |            | limited weighted best effort scheduling                                                                                    |                 |
|----------|---------------|----|------------|----------------------------------------------------------------------------------------------------------------------------|-----------------|
| 11133477 | Not<br>Issued | 30 |            | Method and system for flexible network processor scheduler and data flow                                                   | CHANG, CHIH-JEN |
| 11418606 | Not<br>Issued | 30 | 05/05/2006 | Split socket send queue apparatus and method with efficient queue flow control, retransmission and sack support mechanisms | CHANG, CHIH-JEN |
| 11422087 | Not<br>Issued | 25 |            | MULTIPLE-LEVEL DATA<br>PROCESSING SYSTEM                                                                                   | CHANG, CHIH-JEN |
| 11487265 | Not<br>Issued | 30 | 07/14/2006 | Receive queue device with efficient queue flow control, segment placement and virtualization mechanisms                    | CHANG, CHIH-JEN |
| 11669419 | Not<br>Issued | 30 |            | System and Method for Multicore Communication Processing                                                                   | CHANG, CHIH-JEN |
| 11832220 | Not<br>Issued | 25 | 08/01/2007 | DRAM ACCESS COMMAND<br>QUEUING METHOD                                                                                      | CHANG, CHIH-ÆN  |

Inventor Search Completed: No Records to Display.

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# PALM INTRANET

## Inventor Name Search Result

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| <u></u>      |               |        |            |                                                                                           |                     |  |  |
|--------------|---------------|--------|------------|-------------------------------------------------------------------------------------------|---------------------|--|--|
| Application# | Patent#       | Status | Date Filed | Title                                                                                     | Inventor Name       |  |  |
| 09775443     | 6435419       | 150    | 02/02/2001 | LIQUID AIR FRESHENER<br>DISPENSING DEVICE FOR A<br>DUCT                                   | DAVIS, GORDON<br>D. |  |  |
| 07609903     | Not<br>Issued | 161    | 11/05/1990 | SYSTEM FOR LASER<br>TREATMENT OF THE FEMALE<br>URETHRA AND BLADDER                        | DAVIS, GORDON<br>D. |  |  |
| 07879954     | 5342353       | 150    | 05/06/1992 | SYSTEM FOR LASER<br>TREATMENT OF THE FEMALE<br>URETHRA AND BLADDER                        | DAVIS, GORDON<br>D. |  |  |
| 08297582     | Not<br>Issued | 161    | 08/29/1994 | SYSTEM FOR LASER<br>TREATMENT OF THE FEMALE<br>URETHRA AND BLADDER                        | DAVIS, GORDON<br>D. |  |  |
| 07231102     | Not<br>Issued | 168    | 08/11/1988 | APPARATUS FOR<br>PACKAGING ELECTRONIC<br>CIRCUITS                                         | DAVIS, GORDON<br>H. |  |  |
| 07506510     | Not<br>Issued | 161    | 04/09/1990 | APPARATUS FOR<br>PACKAGING ELECTRONIC<br>CIRCUITS                                         | DAVIS, GORDON<br>H. |  |  |
| 06216450     | 4359146       | 150    | 12/15/1980 | ROTATION SENSING<br>CONTROL                                                               | DAVIS, GORDON<br>J. |  |  |
| 07746558     | D344411       | 150    | 08/19/1991 | WRIST LIGHT MOUNT                                                                         | DAVIS, GORDON<br>P. |  |  |
| 08217252     | Not<br>Issued | 161    | 03/23/1994 | METHOD AND APPARATUS<br>FOR EFFICIENTLY<br>CONNECTING A NETWORK<br>TO A FILE SERVER       | DAVIS, GORDON<br>R. |  |  |
| 10370550     | 7286543       | 150    |            | MEMORY SYSTEM WITH<br>APPARATUS AND METHOD<br>TO ENABLE BALANCED<br>BANDWIDTH UTILIZATION | DAVIS, GORDON<br>T. |  |  |
| 10448528     | 6963868       |        |            | MULTI-BIT PATRICIA TREES                                                                  | DAVIS, GORDON<br>T. |  |  |
| 10453245     | 7149749       | 150    | 06/03/2003 | METHOD OF INSERTING AND                                                                   | DAVIS, GORDON       |  |  |

|          |               |     |            | DELETING LEAVES IN TREE<br>TABLE STRUCTURES                                                                      | T.                  |
|----------|---------------|-----|------------|------------------------------------------------------------------------------------------------------------------|---------------------|
| 10654139 | Not<br>Issued | 89  |            | Data structure supporting random delete and aging/timer function                                                 | DAVIS, GORDON<br>T. |
| 10654502 | Not<br>Issued | 71  | 09/03/2003 | Data structure supporting session timer and variable aging function including self adjustable 2MSL               | DAVIS, GORDON<br>T. |
| 10662007 | Not<br>Issued | 71  | ·          | Apparatus and method for caching lookups based upon TCP traffic flow characteristics                             | DAVIS, GORDON<br>T. |
| 10772737 | Not<br>Issued | 30  | 02/05/2004 | Structure and method for scheduler pipeline design for hierarchical link sharing                                 | DAVIS, GORDON<br>T. |
| 10782617 | Not<br>Issued | 71  |            | Active flow management with hysteresis                                                                           | DAVIS, GORDON<br>T. |
| 10832634 | Not<br>Issued | 93  | 04/27/2004 | METHOD AND SYSTEM FOR<br>COMPRESSING A SEARCH<br>TREE STRUCTURE USED IN<br>RULE CLASSIFICATION                   | DAVIS, GORDON<br>T. |
| 10832957 | Not<br>Issued | 120 |            | Method and system for compressing multi-field rule specifications                                                | DAVIS, GORDON<br>T. |
| 10832958 | Not<br>Issued | 41  |            | Method and system for managing multi-field classification rules relating to ingress contexts and egress contexts | DAVIS, GORDON<br>T. |
| 10868725 | Not<br>Issued | 41  |            | Method and structure for enqueuing data packets for processing                                                   | DAVIS, GORDON<br>T. |
| 10879964 | 7089240       | 150 |            | LONGEST PREFIX MATCH<br>LOOKUP USING HASH<br>FUNCTION                                                            | DAVIS, GORDON<br>T. |
| 10894628 | Not<br>Issued | 30  |            | Multi-field classification dynamic rule updates                                                                  | DAVIS, GORDON<br>T. |
| 10899937 | 7277982       | 150 | 07/27/2004 | DRAM ACCESS COMMAND<br>QUEUING STRUCTURE                                                                         | DAVIS, GORDON<br>T. |
| 11094399 | Not<br>Issued | 71  | 03/30/2005 | Performance of a cache by detecting cache lines that have been reused                                            | DAVIS, GORDON<br>T. |
| 11132656 | Not<br>Issued | 41  | 11 .       | Methods and apparatus for sharing processor resources                                                            | DAVIS, GORDON<br>T. |
| 11132658 | Not<br>Issued | 41  | 05/19/2005 | Methods and apparatus for dynamically switching processor mode                                                   | DAVIS, GORDON<br>T. |
| 11348417 | Not           | 25  | 02/07/2006 | Network congestion detection and                                                                                 | DAVIS, GORDON       |

|                 | Issued        |     |            | automatic fallback: methods, systems & program products                                                    | T.                  |
|-----------------|---------------|-----|------------|------------------------------------------------------------------------------------------------------------|---------------------|
| 11353841        | Not<br>Issued | 30  |            | Longest prefix match lookup using hash function                                                            | DAVIS, GORDON<br>T. |
| 11462404        | Not<br>Issued | 30  |            | METHOD AND STRUCTURE<br>FOR DELETING LEAVES IN<br>TREE TABLE STRUCTURES                                    | DAVIS, GORDON<br>T. |
| 11534711        | Not<br>Issued | 30  |            | Method and Apparatus for Register Renaming in a Microprocessor                                             | DAVIS, GORDON<br>T. |
| 11538445        | Not<br>Issued | 30  |            | Apparatus and Method for Supporting Simultaneous Storage of Trace and Standard Cache Lines                 | DAVIS, GORDON<br>T. |
| 11538458        | Not<br>Issued | 30  |            | Apparatus and Method for Saving Power in a Trace Cache                                                     | DAVIS, GORDON<br>T. |
| 11538831        | Not<br>Issued | 30  | 10/05/2006 | Apparatus and Method for Using Branch Prediction Heuristics for Determination of Trace Formation Readiness | DAVIS, GORDON<br>T. |
| 11559512        | Not<br>Issued | 30  | 1          | Apparatus and Method for Cache Maintenance                                                                 | DAVIS, GORDON<br>T. |
| 11561908        | Not<br>Issued | 30  | 11/21/2006 | Instruction Cache Trace Formation                                                                          | DAVIS, GORDON<br>T. |
| <u>11776162</u> | Not<br>Issued | 19  | 01/01/0001 | MEMORY SYSTEM WITH<br>APPARATUS AND METHOD<br>TO ENABLE BALANCED<br>BANDWIDTH UTILIZATION                  | DAVIS, GORDON<br>T. |
| 11832220        | Not<br>Issued | 25  | 1)         | DRAM ACCESS COMMAND<br>QUEUING METHOD                                                                      | DAVIS, GORDON<br>T. |
| 60301598        | Not<br>Issued | 159 | 06/27/2001 | Ordered semaphore management using the completion unit                                                     | DAVIS, GORDON<br>T. |
| 60301602        | Not<br>Issued | 159 | 06/27/2001 | Network processor with semaphore subsystem                                                                 | DAVIS, GORDON<br>T. |
| 06106824        | 4357678       | 150 |            | PROGRAMMABLE<br>SEQUENTIAL LOGIC ARRAY<br>MECHANISM                                                        | DAVIS, GORDON<br>T. |
| 06194724        | 4362486       | 150 |            | AUTOMATIC MULTILAYER<br>CERAMIC (MLC) SCREENING<br>MACHINE                                                 | DAVIS, GORDON<br>T. |
| 06194727        | 4304536       | 150 | 10/07/1980 | GREEN SHEET SUPPORT FIXTURE SPEED AND POSITION CONTROL SYSTEM FOR A SCREENING MACHINE                      | DAVIS, GORDON<br>T. |

| <u>06876625</u> | <u>5010476</u> | 150 |            | TIME MULTIPLEXED SYSTEM FOR TIGHTLY COUPLING PIPELINED PROCESSORS TO SEPARATE SHARED INSTRUCTION AND DATA STORAGE UNITS | DAVIS, GORDON<br>T. |
|-----------------|----------------|-----|------------|-------------------------------------------------------------------------------------------------------------------------|---------------------|
| 07214250        | 4849706        | 150 | i i        | DIFFERENTIAL PHASE<br>MODULATION<br>DEMODULATOR                                                                         | DAVIS, GORDON<br>T. |
| 07227582        | 4899366        | 150 | 08/02/1988 | TAP ROTATION N FRACTIONALLY SPACED EQUALIZER TO COMPENSATE FOR DRIFT DUE TO FIXED SAMPLE RATE                           | DAVIS, GORDON<br>T. |
| 07227832        | 4991169        | 150 |            | REAL-TIME DIGITAL SIGNAL<br>PROCESSING RELATIVE TO<br>MULTIPLE DIGITAL<br>COMMUNICATION<br>CHANNELS                     | DAVIS, GORDON<br>T. |
| 07254985        | 4972342        | 150 |            | PROGRAMMABLE PRIORITY<br>BRANCH CIRCUIT                                                                                 | DAVIS, GORDON<br>T. |
| 07254986        | 4991133        | 150 |            | SPECIALIZED<br>COMMUNICATIONS<br>PROCESSOR FOR LAYERED<br>PROTOCOLS                                                     | DAVIS, GORDON<br>T. |
| 07289064        | 4871974        | 150 | il ' I     | COHERENT PHASE SHIFT<br>KEYED DEMODULATOR                                                                               | DAVIS, GORDON<br>T. |

| Coanah Amatham Invanta   | Last Name | First Name |        |
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# PALM INTRANET

#### **Inventor Name Search Result**

Your Search was:

Last Name = VERPLANKEN

First Name = FABRICE

| Application# | Patent#       | Status | Date Filed | Title                                                                                                              | Inventor Name          |  |
|--------------|---------------|--------|------------|--------------------------------------------------------------------------------------------------------------------|------------------------|--|
| 07684111     | 5251206       | 150    | 04/12/1991 | HYBRID SWITCHING SYSTEM<br>FOR A COMMUNICATION<br>NODE                                                             | VERPLANKEN,<br>FABRICE |  |
| 08018618     | Not<br>Issued | 166    | 02/17/1993 | HUB AND INTERFACE FOR ISOCHRONOUS TOKEN-RING                                                                       | VERPLANKEN,<br>FABRICE |  |
| 08024061     | 5467359       | 150    | 03/01/1993 | APPARATUS FOR GENERATING AND CHECKING THE ERROR CORRECTION CODES OF MESSAGES IN A MESSAGE SWITCHING SYSTEM         | VERPLANKEN,<br>FABRICE |  |
| 08161354     | Not<br>Issued | 161    | 12/02/1993 | TOKEN STAR SWITCH                                                                                                  | VERPLANKEN,<br>FABRICE |  |
| 08161372     | 5444692       | 250    | 12/02/1993 | TOKEN STAR BRIDGE                                                                                                  | VERPLANKEN,<br>FABRICE |  |
| 08206363     | 5557266       | 150    | 03/07/1994 | SYSTEM FOR CASCADING DATA SWITCHES IN A COMMUNICATION NODE                                                         | VERPLANKEN,<br>FABRICE |  |
| 08234193     | 5561807       | 150    | 04/28/1994 | METHOD AND DEVICE OF<br>MULTICASTING DATA IN A<br>COMMUNICATIONS SYSTEM                                            | VERPLANKEN,<br>FABRICE |  |
| 08547635     | 5794033       | 150    | 10/24/1995 | METHOD AND SYSTEM FOR IN-SITE AND ON-LINE REPROGRAMMING OF HARDWARE LOGICS WITH REMOTE LOADING IN A NETWORK DEVICE | VERPLANKEN,<br>FABRICE |  |
| 08547825     | Not<br>Issued | 166    | 10/25/1995 | EFFICIENT<br>HARDWARE/SOFTWARE<br>INTERFACE FOR A DATA<br>SWITCH                                                   | VERPLANKEN,<br>FABRICE |  |
| ]            |               | i i    |            |                                                                                                                    |                        |  |

| 08547826 | 5666361 | 150 | 10/25/1995 | ATM CELL FORWARDING<br>AND LABEL SWAPPING<br>METHOD AND APPARATUS                                       | VERPLANKEN,<br>FABRICE |
|----------|---------|-----|------------|---------------------------------------------------------------------------------------------------------|------------------------|
| 08547827 | 5668798 | 150 | 10/25/1995 | MULTIPLEXED TC<br>SUBLAYER FOR ATM<br>SWITCH                                                            | VERPLANKEN,<br>FABRICE |
| 08547828 | 5768273 | 150 | 10/25/1995 | METHOD AND APPARATUS<br>FOR PRIORITY LEVEL<br>QUEUEING IN PROCESSING<br>ATM CELL HEADER AND<br>PAYLOAD  | VERPLANKEN,<br>FABRICE |
| 08548030 | 5684797 | 150 | 10/25/1995 | ATM CELL MULTICASTING<br>METHOD AND APPARATUS                                                           | VERPLANKEN,<br>FABRICE |
| 08554113 | 5787071 | 150 |            | HOP-BY-HOP FLOW<br>CONTROL IN AN ATM<br>NETWORK                                                         | VERPLANKEN,<br>FABRICE |
| 08570840 | 5629928 | 150 |            | DYNAMIC FAIR QUEUING TO<br>SUPPORT BEST EFFORT<br>TRAFFIC IN AN ATM<br>NETWORK                          | VERPLANKEN,<br>FABRICE |
| 08579555 | 5687356 | 250 | 12/27/1995 | HUB AND INTERFACE FOR ISOCHRONOUS TOKEN RING                                                            | VERPLANKEN,<br>FABRICE |
| 08635048 | 6038592 | 150 | 04/19/1996 | METHOD AND DEVICE OF<br>MULTICASTING DATA IN A<br>COMMUNICATIONS SYSTEM                                 | VERPLANKEN,<br>FABRICE |
| 08754562 | 5912881 | 150 | 11/21/1996 | METHOD AND APPARATUS<br>FOR FAST CHECKING THE<br>FRAME CHECK SEQUENCE<br>OF A SEGMENTED MESSAGE         | VERPLANKEN,<br>FABRICE |
| 08824941 | 5923664 | 150 | 03/27/1997 | METHOD AND APPARATUS<br>PROVIDING A MULTIPORT<br>PHYSICAL INTERFACE TO<br>HIGH SPEED PACKET<br>NETWORKS | VERPLANKEN,<br>FABRICE |
| 08858156 | 5724348 | 150 | 05/05/1997 | EFFICIENT<br>HARDWARE/SOFTWARE<br>INTERFACE FOR A DATA<br>SWITCH                                        | VERPLANKEN,<br>FABRICE |
| 08943606 | 6044079 | 150 | 10/03/1997 | STATISTICAL PACKET<br>DISCARD                                                                           | VERPLANKEN,<br>FABRICE |
| 08991911 | 6144637 |     |            | DATA COMMUNICATIONS                                                                                     | VERPLANKEN,<br>FABRICE |
| 08993695 | 6003060 | 250 | 12/18/1997 | METHOD AND APPARATUS                                                                                    | VERPLANKEN,            |

|                 |               |     |            | TO SHARE RESOURCES<br>WHILE PROCESSING<br>MULTIPLE PRIORITY DATA<br>FLOWS                              | FABRICE                   |
|-----------------|---------------|-----|------------|--------------------------------------------------------------------------------------------------------|---------------------------|
| 09110917        | 6195335       | 150 | 07/06/1998 | DATA SWITCH                                                                                            | VERPLANKEN,<br>FABRICE    |
| 09110920        | 6370148       | 150 | 07/06/1998 | DATA COMMUNICATIONS                                                                                    | VERPLANKEN,<br>FABRICE    |
| 09477578        | 6539394       | 150 | 01/04/2000 | METHOD AND SYSTEM FOR<br>PERFORMING INTERVAL-<br>BASED TESTING OF FILTER<br>RULES                      | VERPLANKEN,<br>FABRICE    |
| 09706969        | 7061860       | 150 | 11/06/2000 | NETWORK TRAFFIC<br>SHAPING                                                                             | VERPLANKEN,<br>FABRICE    |
| 10248010        | 7103822       | 150 | 12/10/2002 | METHOD AND APPARATUS FOR COMPUTING 'N-BIT AT A TIME' CRC'S OF DATA FRAMES OF LENGTHS NOT MULTIPLE OF N | VERPLANKEN,<br>FABRICE    |
| 10328355        | 6789234       | 150 | 1          | METHOD AND SYSTEM FOR<br>A TIMING BASED LOGIC<br>ENTRY                                                 | VERPLANKEN,<br>FABRICE    |
| 09546651        | 6657962       | 150 |            | METHOD AND SYSTEM FOR<br>MANAGING CONGESTION IN<br>A NETWORK                                           | VERPLANKEN,<br>FABRICE J. |
| <u>09548910</u> | 6862292       | 150 | 04/13/2000 | METHOD AND SYSTEM FOR<br>NETWORK PROCESSOR<br>SCHEDULING OUTPUTS<br>BASED ON MULTIPLE<br>CALENDARS     | VERPLANKEN,<br>FABRICE J. |
| 10068392        | 6757795       | 150 | 02/05/2002 | APPARATUS AND METHOD<br>FOR EFFICIENTLY SHARING<br>MEMORY BANDWIDTH IN A<br>NETWORK PROCESSOR          | VERPLANKEN,<br>FABRICE J. |
| 10301247        | Not<br>Issued | 93  | 11/21/2002 | APPARATUS, METHOD AND<br>PROGRAM PRODUCT TO<br>GENERATE AND USE CRC IN<br>COMMUNICATIONS<br>NETWORK    | VERPLANKEN,<br>FABRICE J. |
| 10772737        | Not<br>Issued | 30  | 02/05/2004 | Structure and method for scheduler pipeline design for hierarchical link sharing                       | VERPLANKEN,<br>FABRICE J. |
| 10850296        | Not<br>Issued | 93  | 05/20/2004 | 3                                                                                                      | VERPLANKEN,<br>FABRICE J. |
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| 11832220        | Not<br>Issued | 25       | 08/01/2007 | DRAM ACCESS COMMAND<br>QUEUING METHOD                                                                                  | VERPLANKEN,<br>FABRICE J.   |
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